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SC12866TP


PTO/SB/08A (04-03)

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		Application Number	10/662832
		Filing Date	
		First Named Inventor	Michael J. Rendon et al.
		Group Art Unit	284
Examiner Name		Attorney Docket Number	SC12866TP
Sheet	1	of	1

U. S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
EJ	AA	US -2003/0042551	03-06-2003	Agnello et al.	
	AB	US -2002/0197806	12-26-2002	Furukawa et al.	
	AC	US -6,472,283	10-29-2002	Ishida et al.	
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	AE	US -6,187,620	02-13-2001	Fulford et al.	
	AF	US -6,104,063	08-15-2000	Fulford et al.	
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FOREIGN PATENT DOCUMENTS							
Examiner r Initials*	Cite No. ¹	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
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NON PATENT LITERATURE DOCUMENTS			
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EJ	AM	VAN MEER, Hans et al.; "70 nm Fully-Depleted SOI CMOS Using a New Fabrication Scheme: The Spacer/Replacer Scheme"; Symposium on VLSI Technology Digest of Technical Papers; 2002; 2 pp; IEEE	

Examiner Signature		Date Considered	9-1-04
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